

Product Specification Sheet

Model: MS3740

740 MS3700

Slim Plug-In Signal Reverser with Isolated Single/Dual Output

DESCRIPTION

The MS3740 is a slim, plug-in signal reverser that converts DC current or voltage input signals into DC signals inversely proportional to those input signals and provides isolated single or dual output.

ORDERING CODE

ORDERING CODE		
Model -	1S3740 - 🗆 - 🗆 🗆 🗆	
Power Supply A: 100 to 240V AC (50 to 60 D: 24V DC P: 10		
H : 10 to 50mA DC	3: 0 to 1V DC 4: 0 to 10V DC 5: 0 to 5V DC 6: 1 to 5V DC 4W: ±10V DC 5W: ±5V DC 0: Other DC voltage signal	
Output 1 A: 4 to 20mA DC D: 0 to 20mA DC Z: Other DC current signal	1: +10 to 0mV DC 2: +100 to 0mV DC 3: +1 to 1V DC 4: +10 to 0V DC 5: +5 to 0V DC 6: +5 to +1V DC 3W: +1 to -1V DC 4W: +10 to -10V DC	

Output 2 — No code: None

The codes are the same as for Output 1.

5W: +5V to -5V DC **0**: Other DC voltage signal

Note 1: When a voltage output is selected for Output 1, a current output cannot be selected for Output 2.

Note 2: When the code A (4 to 20mA) is selected for both of the two outputs, the output load will be 550Ω maximum for Output 1 and 350Ω maximum for Output 2.

Options

No code: None

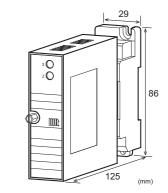
/K: Fast response (0 to 90% response time: 10ms max.)

/L: Dual current output with high output load

* Not subject to CE approval. (OUT-1: 750Ω / OUT-2: 550Ω)

/X: Others (Special order)

* For non-standard options, ask MTT for availability.



ORDERING INFORMATION

To place an order, please use the ordering code format as shown on the left. (e.g.) MS3740-A-AA6

Other Ordering Examples:

For an input code of "Z": MS3740-A-ZAA (Input: 8 to 20mA)

20mA)

For an output code of "0": MS3740-A-A60 (Output: 5 to 2V) For an option code of "X": MS3740-A-66/X (Response

frequency: 50Hz)

 $C \in$

Note: If you wish to include multiple options in your order, specify the option codes in series (e.g. /KX).

SPECIFICATIONS

POWER SECTION			
Power	100 to 240	100 to 240V AC: 85 to 264V AC (47	
Requirements	to 63Hz)	to 63Hz)	
	24V DC: 2	24V DC±10%	ó
	100 to 240	V DC: 85 to	264V DC
Power Sensitivi	ty Better than	Better than ±0.1% of span for each	
	power sup	ply range.	
Power Line Fus	se 160mA fu	160mA fuse is installed (standard).	
Power Consumption			
Power	100-240V AC	24V DC	100-240V DC
Single Output	4.0VA max	1.2W max	4.8W max
Dual Output	5.0VA max	1.5W max	6.0W max

OINPUT SECTION

•		
Input Resistance		
Voltage Input (DC)	With or without power: $1M\Omega$ min.	
Current Input (DC)	4 to 20 mA (std.) 250Ω	
	2 to 10mA	250Ω
	1 to 5 mA	100Ω
	0 to 20mA	250Ω
	10 to 50mA	10Ω
Allowable Input Voltage		
Voltage Input Model	30V DC max., conti	inuous. (Standard
	for a span up to 10V	7)
Current Input Model	40mA DC max., cor	ntinuous.
	(Standard for 4 to 20	0mA)



Ranges Available		
	Current Signal	Voltage Signal
Input Range (DC)	-100 to 100mA	-300 to 300V
Input Span (DC)	$100 \mu A^{*1}$ to $200 mA$	200mV*2 to 600V
Input Bias	-100 to 100%	-100 to 100%
Note: For any input range including negative input signals,		
the input spans for current and voltage signals range		

from (*1)200µA to 200mA and (*2)400mV to 600V, respectively.

Input Spec. Ex.1: For 3 to 8V input, the input span is 5V and the bias +60%.

Input Spec. Ex. 2: For -5 to 0V input, the input span is 5V and the bias -100%.

OUTPUT SECTION

• • • • • • • • • • • • • • • • • • • •			
Allowable Output L	Allowable Output Load		
Voltage Output	1V span and up	2mA max.	
(DC)	10mV	10 k Ω min.	
	100mV	100 k Ω min.	
Current Output	4-20mA single output	it 750Ω max.	
(DC)	4-20mA dual output	Output 1:	
		550Ω max.	
		Output 2:	
		350Ω max.	
Zero Adjustment	Approx. ±5% of span.		
	(Adjustable by the front-accessible		
	trimmer.)		
Span Adjustment	Approx. ±5% span.		
	(Adjustable by the front-accessible		
	trimmer.)		
Ranges Available			
	Current Signal	Voltage Signal	
Output Range (DC)	0 to 20mA	-10 to 10V	
Output Span (DC)	4 to 20mA	10mV to 20V	
Output Bias	0 to 100%	-100 to 100%	
* For aureant auteut signals, the accuracy of any aureant			

* For current output signals, the accuracy of any current output smaller than 0.1mA is not guaranteed.

Output Spec. Ex.1: For 4 to 20mA output, the output span is 16mA and the bias +25%.

Output Spec. Ex. 2: For -1 to 4V output, the output span is 5V and the bias -20%.

PERFORMANCE

Better than ±0.1% of span (at 25°C±5°C).
Better than ±0.2% of span per 10°C
change in ambient.
85ms max. (0 to 90%) with a step
input at 100%.
100dB min. (500V AC, 50/60Hz)
4-way isolation between input, output
[Output 1/Output 2], power, and
ground.
100MΩ min. (@ 500V DC) between
input, output [Output 1/Output 2],
power, and ground.
Input / Output [Output 1/Output 2] /
[Power, Ground]: 2000V AC for 1
minute (Cutoff current: 0.5mA)
Power / Ground: 2000V AC for 1
minute (Cutoff current: 5mA)
Output 1 / Output 2: 500V AC for 1
minute (Cutoff current: 0.5mA)
Tested as per ANSI/IEEE
C37.90.1-1989.

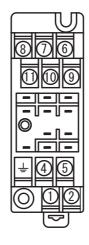
Operating	Ambient temperature: -5 to 55°C
Environment	Humidity: 5 to 90% RH
	(non-condensing)
Storage	-10 to 60°C
Temperature	
6 DUIVOIO 41	
●PHYSICAL	
Installation	Wall/DIN rail mounting
Wiring	M3.5 screw terminal connection
	(with a power terminal block cover &
	drop-out prevention screws)
Screwing Torque	0.8 to 1.0 [Nm] * Recommended
External	$W29 \times H86 \times D125$ mm
Dimensions	(including the mounting screw and
	socket)
Weight	Main unit: 120g max.
	Socket: 80g max.
• MATERIAL C	
• MATERIALS	170 : (77.047.0)
Housing	ABS resin (UL 94V-0)
Terminal Block	PBT resin (UL 94V-0)
Terminal Block	PC resin (UL 94V-2)
Cover	
DIN Rail Stopper	PP resin (UL 94HB)
Screw Terminal	Nickel-plated steel
Contacts Material	Brass with 0.2µm gold plating
and Finish	
Printed Circuit	Glass fabric epoxy resin
Board	(FR-4: UL 94V-0)
Anti-Humidity	HumiSeal® 1A27NS (Polyurethane)
Coating	
dodding 1®:	

^{*} HumiSeal® is a registered trademark of Chase Corporation.

STANDARDS CONFORMITY

EMC Directive (2014/30/EU)	
EN61326-1: 2013	
Low Voltage Directive (2014/35/EU)	
IEC61010-1/EN61010-1: 2010	
Installation Category II	
Pollution Degree 2	
Maximum operating voltage 300V	
Reinforced insulation between	
[input/output/GND] and power.	

TERMINAL ASSIGNMENT



1	P (+)	POWER
2	N (-)	FOWER
Ţ	GND	
4	+ OUTF	PUT 1
(5)	- OUTP	UT 1
6	N.C.	
\bigcirc	+ OUTF	PUT 2
8	- OUTP	UT 2
9	+ INPU	Т
10	- INPUT	<u> </u>
11	N.C.	

BLOCK DIAGRAM

