DESCRIPTION

The MS3700DV is a slim plug-in voltage divider that divides high voltage direct current signals and provides a single output.

ORDERING CODE

MS3700DV - ☐

Model

Division Ratio
1: 1/600 (Standard)
2: Other division ratio

Options
No code: None
/Z: Allowable input voltage: 1200V max.
/X: Special order
* For non-standard options, ask MTT for availability.

ORDERING INFORMATION

To place an order, please use the ordering code format as shown above.
(e.g.) MS3700DV-1

SPECIFICATIONS

●INPUT SECTION

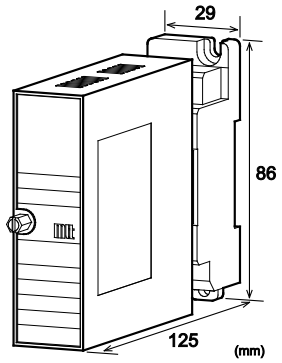
Input Resistance	Approx. 1.2MΩ (Standard)
Allowable Input Voltage	±600V DC, continuous.
Ratios Available	1/300 to 1/1000 (Standard: 1/600)

●OUTPUT SECTION

Output Resistance	Approx. 2kΩ (Standard)
Output Voltage	Input voltage × Division ratio

●PERFORMANCE

Accuracy Rating	Better than ±0.2% of span (at 25°C±5°C).
Temperature Effect	Better than ±0.05% of span per 10°C change in ambient.
Insulation Resistance	100MΩ min. (@ 500V DC) between [input/output] and ground.
Dielectric Strength	2100V AC for 1 minute between [input/output] and ground. (Cutoff current: 0.5mA)
Operating Environment	Ambient temperature: -5 to 55°C Humidity: 5 to 90% RH (non-condensing)
Storage Temperature	-10 to 60°C



●PHYSICAL

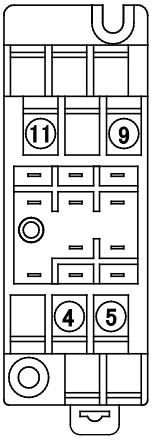
Installation	Wall/DIN rail mounting
Wiring	M3.5 screw terminal connection (with a power terminal block cover & drop-out prevention screws)
Screwing Torque	0.8 to 1.0 [Nm] * Recommended
External Dimensions	W29 × H86 × D125mm (including screws and socket)
Weight	Main unit: 70g max. Socket: 60g max.

●MATERIALS

Housing	ABS resin (UL 94V-0)
Terminal Block	PBT resin (UL 94V-0)
Terminal Block Cover	PC resin (UL 94V-2)
DIN Rail Stopper	PP resin (UL 94HB)
Screw Terminal	Nickel-plated steel
Contacts Material and Finish	Brass with 0.2µm gold plating
Printed Circuit Board	Glass fabric epoxy resin (FR-4: UL 94V-0)
Anti-Humidity Coating	HumiSeal® 1A27NS (Polyurethane)

* HumiSeal® is a registered trademark of Chase Corporation.

TERMINAL ASSIGNMENT



④	+ OUTPUT 1
⑤	- OUTPUT 1
⑨	+ INPUT
⑪	- INPUT

BLOCK DIAGRAM